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### 54 Apparatus and method for thick wafer measurement.

57 The thickness of the entire surface of a thick semiconductor (94) layer on an insulator is determined all at once by dividing the surface into a plurality of pixels, by varying the length of a variable length optical path (110) in a Michelson interferometer (66) having an infrared source (62), illuminating the entire surface of the layer with an image of the IR source during the length variation operation, and by detecting (64) the product of the intensity ( $I(z)$ ) from the Michelson interferometer and the reflectivity  $R(t)$  of the layer as the path length is varied, such being indicative of the thickness of the layer, and by determining, for each pixel, which one of a plurality of groups of stored reflectance values, for a corresponding plurality of thicknesses, best matches the detected values, thereby providing a thickness map of the semiconductor layer.

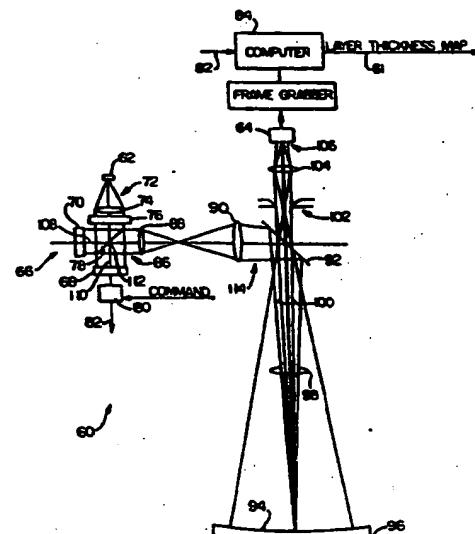


FIG2

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Cross Reference to Related Applications

This application discloses subject matter that may be disclosed and claimed in copending applications having the following Serial Numbers, filing dates and titles:

5 (1) Serial No. 07/804,872 (filed on December 6, 1991) entitled "Apparatus and Method for Measuring the Thickness of Thin Films";  
 (2) Serial Number 07/906,079 (filed June 29, 1992) entitled "Apparatus and Method for Performing Thin Film Layer Thickness Metrology on a Thin Film Layer Having Shape Deformations and Local Slope Variations";  
 (3) Serial Number 07/891,344 (filed May 29, 1992) entitled "Apparatus and Method for Performing Thin Film Layer Thickness Metrology By Deforming A Thin Film Layer Into A Reflective Condenser"; and  
 10 Serial Number (Atty. Docket No. D92020) (filed December 10, 1992) entitled "Apparatus and Method for Measuring the Thickness of Thin Films" and assigned to the assignee hereof and which are hereby expressly incorporated by reference.

15 BACKGROUND OF THE INVENTION

The present invention relates to an apparatus and method for measuring the thickness of a semiconductor such as the outer layer of a semiconductor-on-insulator wafer for thicknesses of more than five microns and up to many hundreds of microns.

20 There are no accurate full aperture imaging systems which can measure very thick layers of silicon in a bonded wafer and provide a thickness map of the outer layer. Commercial instruments operate one point at a time on the wafer in either the ultraviolet or visible region and cannot measure films hundreds of microns thick since the high absorption in the silicon outer layer effectively eliminates fringe formation.

25 Even though infrared radiation is not absorbed as much, it is difficult to achieve a plurality of narrowband infrared filters such as would be required to expose the wafer separately to many different infrared wavelengths (as was similarly done in the above mentioned copending application Serial No. 07/804,872) with visible light for relatively thin layers of silicon on insulator, i.e., on the order of four or five microns or even tens of microns thick.

30 Semiconductor manufacturers presently purchase silicon wafers that have been micropolished down to the four-to-five micron thickness range. The above cited patent application permits smoothing down to less than 100 nanometer with uniformity better than 10 percent. Nevertheless, it has heretofore been required to purchase wafers already micromachined down to the four-to-five micron thickness level as a starting point. This is due to the inability of the visible light system disclosed in the above cited copending patent application to create a thickness map for wafers in the hundreds of micron thickness range for the reason of the absorption problems mentioned above.

35 It would be desirable to purchase thick wafers to begin with and be able to create a thickness map thereof so that high rate plasma assisted chemical etching (PACE) polishing might be used to remove the bulk of the layer now removed by conventional grinding and chemo-mechanical polishing technologies. If such thickness maps of thick silicon layers could be produced quickly (approximately one minute per map), then not only can less expensive starting wafers be used, but the map would allow partial smoothing of the outer film as the bulk of the layer is removed by a high rate plasma assisted chemical etching polishing process. The final thickness target would be in the four-to-five micron range and the additional smoothing afforded by the high rate PACE process would reduce local slopes and subsequently allow precise measurement with the visible metrology system disclosed in the above cited copending application.

45 SUMMARY OF THE INVENTION

An object of the present invention is to provide means and methods for measuring the outer semiconductor layer of a semiconductor-on-insulator wafer for film thicknesses on the order of hundreds of microns.

50 According to a method of present invention, the semiconductor layer of a semiconductor-on-insulator (wafer), wherein the semiconductor layer has a thickness of more than five or even hundreds of microns, has its thickness measured by combining an image of an IR source in a Michelson interferometer with a reflection of

(1) European 9231118.1;  
 (2) European 93305095.7;  
 (3) European 93303721.0

the IR source provided by the wafer over a selected variation in the optical path length of the Michelson interferometer. The combination will yield a Fourier burst at a path length corresponding to the thickness of the layer.

According to an apparatus of the present invention, an instrument for measuring the outer semiconductor layer of a semiconductor-on-insulator wafer comprises an IR source such as a Nernst glover and a Michelson interferometer combined with a reflective optical system that reflects a beam from the Michelson interferometer onto the wafer and provides a thickness image of all the pixels in the form of a thickness pattern that varies according to various mirror displacements of the interferometer. An infrared camera, for example, for the 3 to 5 micron region may be provided to capture the reflectance information from the wafer. A computer stores the images at each position of the mirror in the Michelson interferometer so that, for each pixel, the image at that mirror position may be compared to a plurality of groups of calculated values or groups of calibration values for determining the thickness by finding a best group fit.

The present invention applies to the measurement of thick, outer layers of semiconductors such as silicon in a semiconductor-on-insulator wafer such as silicon-on-insulator (SOI). Such an insulator may be silicon dioxide. The invention can also be used in measuring the thickness uniformity of silicon, germanium wafers and any other infrared transmitting material. A great deal of prior art exists in Fourier transform spectroscopy. However, imaging for transform infrared spectroscopy technology is not generally available and certainly no commercial measuring system exists to date. The present invention enables thickness maps of thick silicon layers to be produced in a timely manner, i.e., at less than one minute per map. Not only can less expensive starting wafers be used, but the map allows partial smoothing of the outer film as the bulk of the layer is removed by a high rate PACE polishing process. A final thickness in the range of four-to-five microns is achieved and additional smoothing may be achieved using the teachings of the above cited copending application 07/804,872 by the high rate PACE process to reduce local slopes and subsequently allow precise measurement with a visible metrology system.

These and other objects, features and advantages of the present invention will become more apparent in light of the following detailed description of a best mode embodiment thereof, as illustrated in the accompanying drawing.

#### BRIEF DESCRIPTION OF THE DRAWING

Fig. 1 shows a visible light metrology system for measuring thin film semiconductor layers, according to a copending application cited herein and incorporated by reference; Fig. 2 shows an apparatus, according to the present invention, using a Michelson interferometer for measuring thick semiconductor layers on an insulator for film thicknesses of between five and many hundreds of microns; Fig. 3 shows a method, according to the present invention, for determining layer thickness; Fig. 4 shows layer signatures for various film thicknesses in an SOI wafer, according to the present invention; and Fig. 5 shows image acquisition and computation of a merit function, according to the present invention.

#### DETAILED DESCRIPTION OF THE INVENTION

Fig. 1 shows an apparatus 10 which is a simplified version of a similar apparatus disclosed in the above cited copending application having U.S. Serial No. 07/804,872. The invention disclosed in that application included an electro-optical system for measuring the outer silicon layer thickness of a silicon on insulator (SOI) semiconductor wafer. The instrument measures the surface reflectance of the wafer over a full aperture at several discrete wavelengths and computes the outer film thickness map either by numerical iteration or by the use of a calibration wafer. This thickness map can be subsequently used by a micropolishing system such as a plasma assisted chemical etching (PACE) system to thin the outer layer to 1,000 Å average thickness to an accuracy of 100 Å.

For that case, the choice of the optical system was the key to obtaining the high contrast ratio spectral mapping data at high speed. Prior art systems use a focused spot or illuminated fiber bundle at the wafer surface followed by a grating or prism spectrograph to measure the surface spectral reflectance variations. Surface mapping is obtained in the prior art by moving the wafer, by scanning a line image, or using an array of fiber bundles operating as localized illuminators/collectors. In all cases, this reflectance data must then be numerically corrected due to variations in angle of incidence caused by the illuminator beam aperture number.

As disclosed in the copending application Serial No. 07/804,872, a better method is to illuminate with a lamp such as a halogen lamp 12 the entire wafer 14 and perform all imaging in collimated light 16 so that the angle of incidence over the entire wafer is zero. This results in the most compact optical system 10 since field

angles are insignificant and also allows significantly simpler (and faster) measurement of the reflectance/thickness map by using either numerical computations or taking spectral measurement from a calibration wafer. High overall speed is attained by measuring the full aperture of the wafer with a CCD camera 18, thereby eliminating any wafer motion during data acquisition. Reflectance maps are obtained at different wavelengths by using a series of narrowband filters on a filter wheel 20 in collimated light 22 (3250 Å half bandwidth) rotating at typically 60 RPM. The wheel may include, for example, up to 20, approximately 2 centimeter diameter filters. The use of collimated light at the narrow band filter also produces the narrowest spectral bandwidth (high fringe visibility) and simplifies data reduction. The optical system can be configured to measure thin film thicknesses by using combinations of different wavelengths, different fixed angles and polarization states.

The white light source 12 for the instrument 10 may comprise a circular aperture illuminated either by a halogen lamp and condensing lens 24 or by a fiber optic light source. The size of the aperture determines the field angles in the collimated sections of the optical system and the orientation is chosen to allow the return aperture image to be folded out to the CCD camera 18. The white light source is spectrally filtered by a series of narrowband filters 20, nominally 30 to 50 Å half bandwidth placed in the collimated beam formed by a lens 26 responsive to light from a slit source 28 where the slit is perpendicular to the illustration of Fig. 1. The set of filters 20 is placed around the periphery of a moving filter wheel 30 having an axis 32 for rotating, for example, at 60 RPM for illuminating the wafer 14 in rapid succession with monochromatic light of different wavelengths. An electronic signal on a line 34 is generated by the filter wheel to serve as a read-cut time reference for the digitizing system attached to the CCD camera 18. Locating the filter in a collimated section 22 minimizes spectral broadening of the filter shape caused by the different field angles defined by the primary slit size.

A second lens 36 relays the monochromatic image of the primary aperture to the focal plane of the objective lens 38, which collimates the light 16 and illuminates the full aperture of the SOI wafer which may, for example, have a diameter of 100mm. The reflected light returns through the same lens 38 and forms an image alongside the original. Separation of the two images can be effected by a small lateral shift in the optical axis of the lens 38 with respect to the optical axis of the lens 24, 26, 36. Equivalently, the wafer can be tilted through a small angle less than one degree to achieve the same effect. Or, an on-axis beam splitter can be used. A small pick-off mirror 40 is used to redirect the reflected light from the wafer to a final lens 42, which again forms a collimated beam of a diameter which matches the shortest side of a CCD detector array (typically 6.3mm for a 1.69 centimeter commercial camera format). Additionally, a real image 44 of the wafer is formed in a collimated light space 46 and the CCD camera focal plane is placed in this position. It should be noted that variations of the above described apparatus 10 are certainly possible and are described in some detail in the above cited copending patent application Serial No. 07/804,872. That application also shows in Figs. 3 and 4 thereof the surface of such a wafer including division into pixels as described in the text thereof at page 9, lines 15-25 and page 11, lines 3-27.

A numerical method of measurement of the outer film thickness which varies over the surface of the wafer 14 is also described in the copending patent application Serial No. 07/804,872 and comprises the assumption of values for the thin film constants and calculating spectral reflectances, at a predefined set of wavelengths, for the two-film system. An SOI semiconductor wafer consists of a silicon/silicon dioxide/silicon (Si/SiO<sub>2</sub>/Si) sandwich structure fabricated by growing silicon dioxide on two wafer surfaces and bonding the two silicon dioxide faces together at high temperature. One of the outer silicon faces of the sandwich is then mechanically ground and polished to an average thickness of several microns. This polishing process is unfortunately quite expensive and adds considerable value to the silicon wafers purchased prior to being processed by the system of Fig. 1. The etalons purchased by the pre-processors for mechanical grinding or polishing are typically grown to thicknesses much greater than several microns. As discussed above, these may have thickness measurements of 100 or several hundred microns. Unfortunately, as also suggested above in the Summary of the Invention, the system of Fig. 1 provides light that is heavily absorbed by silicon at such thicknesses. As also mentioned above, it would be advantageous to be able to skip the mechanical grinding and polishing from several hundred microns down to several microns.

Fig. 2 shows an instrument 60, according to the present invention, that may be used to measure the outer silicon layer of a silicon-on-insulator (SOI) wafer for film thicknesses greater than five microns and up to many hundreds of microns. The instrument of Fig. 2 is an accurate, full aperture imaging system which can measure very thick layers of silicon (or germanium and other materials transmissive to IR) in a bonded wafer and provide a thickness map on a signal line 61 of the outer layer. The process can also be applied to thick layers of free standing materials such as silicon where both sides are polished. Present day commercial instruments operate in the UV or visible region and cannot measure films hundreds of microns thick since the high absorption in the silicon outer layer effectively eliminates fringe formations. Moreover, such commercial instruments only operate one pixel at a time. The instrument of Fig. 2 combines the high speed concept of the instrument of Fig. 1 as disclosed in the above cited copending patent application having U.S. Serial No. 07/804,872 with a

several signal bursts occur at multiples of the primary translation distance corresponding to multiple bounces within the layer. However, it will be observed, that the burst corresponding to the actual thickness will have a higher amplitude.

The interferometric signature expected for a uniform layer would be a rapid fluctuation in intensity of the whole image as the mirror 68 is moved through the point where the optical path difference of the two mirrors 68, 70 matches the optical path difference between the two silicon surfaces forming the thick film. In the case of a non-uniform silicon thickness, translation of the mirror 68 causes shadows or fringes to move across the full image of the wafer 94. The measurement of the images thus produced requires more or less continuously or intermittently capturing images at the focal plane 106 of the camera 64 of the wafer as the mirror 68 is scanned from zero path difference to more than 3.45 times the expected thickness of the silicon film and then determining, for each pixel, the mirror position that produces a burst for that pixel. This image acquisition step is illustrated in a step 120 in Fig. 5. The mirror displacement that produces a maximum burst for a given pixel corresponds to the thickness of the film at that point on the wafer.

The resolution of the thickness measurement can be enhanced by carefully measuring the amplitude variations of the burst signature and estimating or finding the maximum. This can be done by full knowledge of the reflectance characteristics of silicon over the wavelengths of interest and matching the detected signatures thereto by computing a merit function comparison similar to that disclosed in the above cited copending application U.S. Serial No. 07/804,872. Such a merit function draws from a library of precalculated reflectances for silicon at the various wavelengths and forms, for each sample reflectance for each pixel, a difference between each value in the library which is then squared and summed for that reflectance sample. Such a summation is done for each sample for each pixel and the merit function having the smallest difference is the one having the closest comparison and is selected as being the one corresponding to the thickness of the layer for that pixel.

In other words, a library of burst signals ( $S(z, t_2)$ ) for an SOI wafer illuminated by a Michelson interferometer is obtained by evaluating the integral:

$$S(z, t_2) = \int_{\lambda_1}^{\lambda_2} R(\lambda, n_0, k_0, n_1, k_1, t_1, n_2, k_2, t_2; H(\lambda, z)) d\lambda$$

where  $\lambda_1, \lambda_2$  are the spectral wavelength limits of the instrument,  $R(\lambda, n_0, k_0, n_1, k_1, t_1, n_2, k_2, t_2)$  is the reflectance of the SOI wafer and  $H(\lambda, z)$  is the transmission of the Michelson interferometer, which illuminates the wafer. The parameter  $z$  is the difference in path length between the two arms of the interferometer. The reflectance of the SOI wafer is determined by the optical constants  $n_0, k_0$ , of the substrate and the optical constants  $n_1, k_1, t_1$  and  $n_2, k_2, t_2$  of the two films of thickness  $t_1$  and  $t_2$  in the SOI structure. It should be realized that such a library may be constructed from a calibration wafer in a manner similar to the method shown in connection with Fig. 4 of copending application Serial No. 07/804,872. In either event, such a library is retrieved, as shown in a step 122 in Fig. 5 for each pixel.

A merit function  $MF_t$  is constructed, as shown in a step 124, between a set of library burst signals  $S(z, t_2)$  for a thickness  $t_2$  and a set of measured data  $P(z)$  at a set of distances  $z_1, \dots, z_n$ , i.e.,

$$MF_t = \sqrt{\sum_{i=1}^n (S(z_i, t_2) - P(z_i))^2}$$

Clearly, if the measured and precalculated signals match exactly at some thickness value  $t_2$ , then the merit function will be zero. In practice, the data contains noise and a zero value is not obtained. In this case the minimum value is sought by starting with a set of distances  $z_1, \dots, z_n$ , computing the merit functions for all values of  $t_2$  and then progressively increasing the distance values to  $z_1 + \Delta, z_2 + \Delta$  where  $\Delta$  is the smallest resolvable increment of the mirror drive. The set of merit functions for each set of distances is computed continuously until a minimum merit function is obtained, as indicated in a step 126, at which point the value of  $t_2$  is determined.

In a similar way, the algorithm can be used to determine the thickness  $t_1$  of a free standing polished slab of silicon or other IR transmitting material by generating libraries for values of  $t_1$  with  $t_2=0.0$  and  $n_0=1.0$  and  $k_0=0.0$  (air).

This and other equivalent techniques may be used to find the maximum of the layer signature in order to find the thickness with a high degree of accuracy.

Although the invention has been shown and described with respect to a best mode embodiment thereof, it should be understood by those skilled in the art that the foregoing and various other changes, omissions and additions in the form and detail thereof may be made therein without departing from the spirit and scope of the invention.

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### Claims

1. A method for determining thickness of a semiconductor layer, comprising the steps of:
  - 10 combining an input light image of an infrared source with an output reflected light image of the source from the semiconductor layer which input and output light images traverse a variable length image path; and
    - 15 detecting, for each of a plurality of areas of a surface of the semiconductor, a signal burst in the combined input and output light images at a discrete length in the variable length image path corresponding to the thickness of the semiconductor layer.
2. The method of claim 1, further comprising the steps of:
  - 20 for each area, sampling the magnitude of the signal burst at a plurality of selected spaced apart sample points in proximity to the detected discrete length; and
    - 25 comparing the sampled magnitudes of the signal burst to signal magnitudes corresponding to similarly spaced apart samples of a plurality of known thickness semiconductor layers for matching one of the plurality of known layer thickness samples to the signal burst sample points for determining the thickness.
3. The method of claim 2, wherein, for each area, the step of comparing comprises the steps of:
  - 30 comparing a plurality of groups of stored samples of known thickness layers to the signal burst sample points; and
    - 35 finding a group of stored samples that more closely matches the signal burst samples than any other group.
4. A method for determining thickness of a semiconductor layer, comprising the steps of:
  - 40 providing an image of an infrared light source to a beamsplitter in an interferometer having a stationary mirror and a movable mirror that is movable from a first point to a second point;
    - 45 moving the movable mirror from the first point to the second point;
      - 50 providing, as the movable mirror moves, an input light image of the source from the interferometer to surface boundaries of the semiconductor layer for reflection therefrom as reflected, output light;
        - 55 receiving, in response to the output light, as the movable mirror moves, images of the source reflected from the boundaries;
          - 60 determining the thickness of the semiconductor layer for each of a plurality of separate areas of the semiconductor layer according to a discrete position of the movable mirror corresponding to a detection of a signal burst in the reflected output light for each separate area.
  5. Apparatus comprising:
    - 65 an infrared (IR) source for providing an IR source signal;
      - 70 a Michelson interferometer having a variable optical path length, responsive to the IR source signal, for providing images of the IR source signal at various lengths of the optical path;
        - 75 means, responsive to the images of the IR source signal, for combining, for each length of the optical path, the image of the IR source signal and a reflection thereof from a semiconductor layer; and
          - 80 means, responsive for each length of the optical path to the combined image of the IR source signal and its reflection for each of a plurality of areas of the layer, for providing a layer thickness map signal indicative of the thickness of the layer for each of the plurality of areas.
    6. The apparatus of claim 5, wherein the Michelson interferometer comprises:
      - 85 a beamsplitter, responsive to the IR source signal, for providing a first reflected signal along an axis and for providing a transmitted signal along the variable optical path length axis;
        - 90 a stationary mirror, responsive to the first reflected signal along the axis and to a transmitted reflection of the IR source from the semiconductor layer along the axis, for providing a second reflected signal along the axis;

5 a movable mirror, responsive to the transmitted signal along the variable optical path length axis and to a reflection of the reflection of the IR source from the semiconductor layer along the variable optical path length axis, for providing a third reflected signal along the variable optical path length axis; and wherein

6 the beamsplitter combines the second reflected signal along the axis and the third reflected signal along the variable optical path length axis, for providing the images of the IR signal at various lengths of the variable optical path length axis.

10 7. The apparatus of claim 5, further comprising lens means, responsive to the images of the IR signal at the various lengths of the optical path, for providing a collimated image signal to the means for combining the image of the IR source signal and its reflection from the semiconductor layer.

8. The apparatus of claim 5, wherein the plurality of areas are illuminated at the same time.

15 9. The apparatus of claim 5, further comprising:

an aperture, responsive to the combined image of the IR source signal and its reflection, for providing a selected portion thereof to

a lens for providing the selected portion of the combined image of the IR source signal and its reflection to

20 an infrared camera for recording the combined image of the IR source and its reflection for providing same to the means.

10. The apparatus of claim 9, further comprising a frame grabber, responsive to images recorded in the camera for providing same to the means.

25 11. The apparatus of claim 5, further comprising means for providing a signal having a magnitude indicative of the length of the variable optical path length and wherein the means is responsive to the length signal for correlating each length of the optical path to the combined image of the IR source signal and its reflection.

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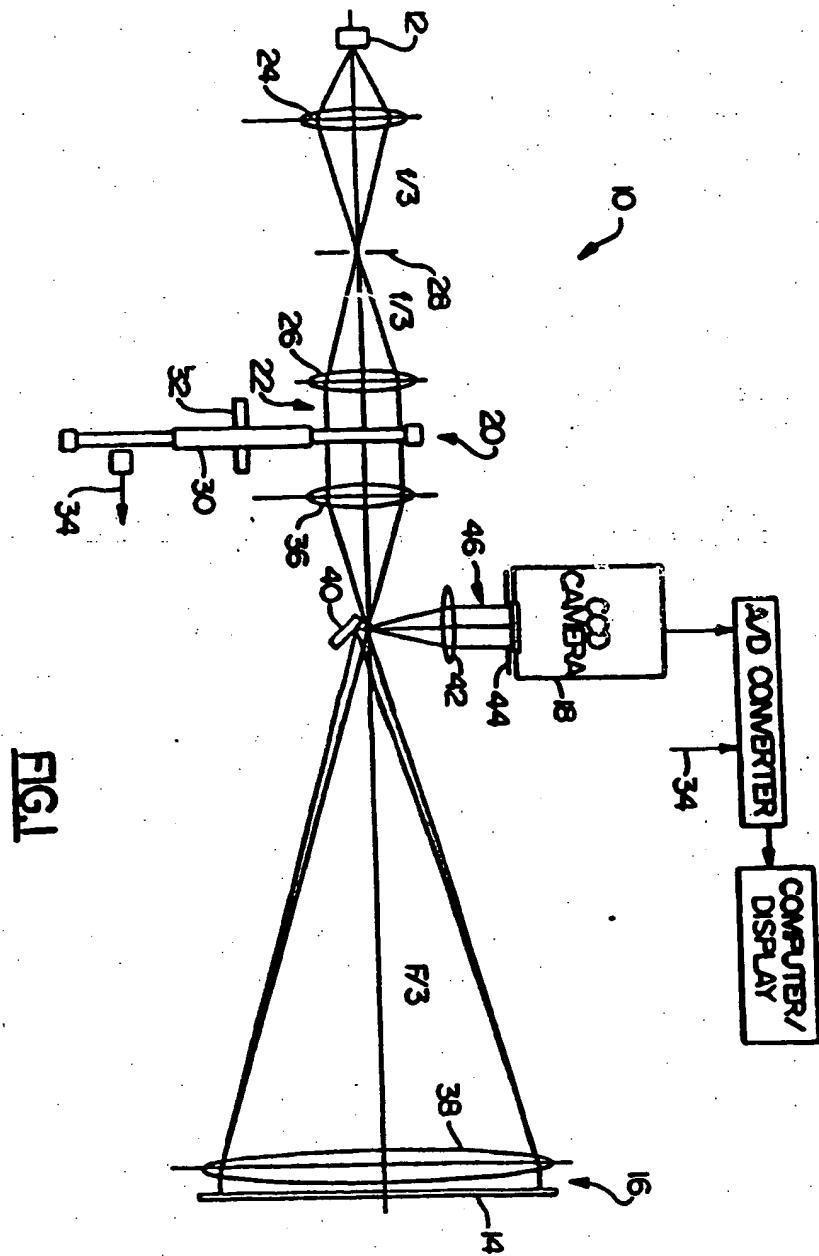
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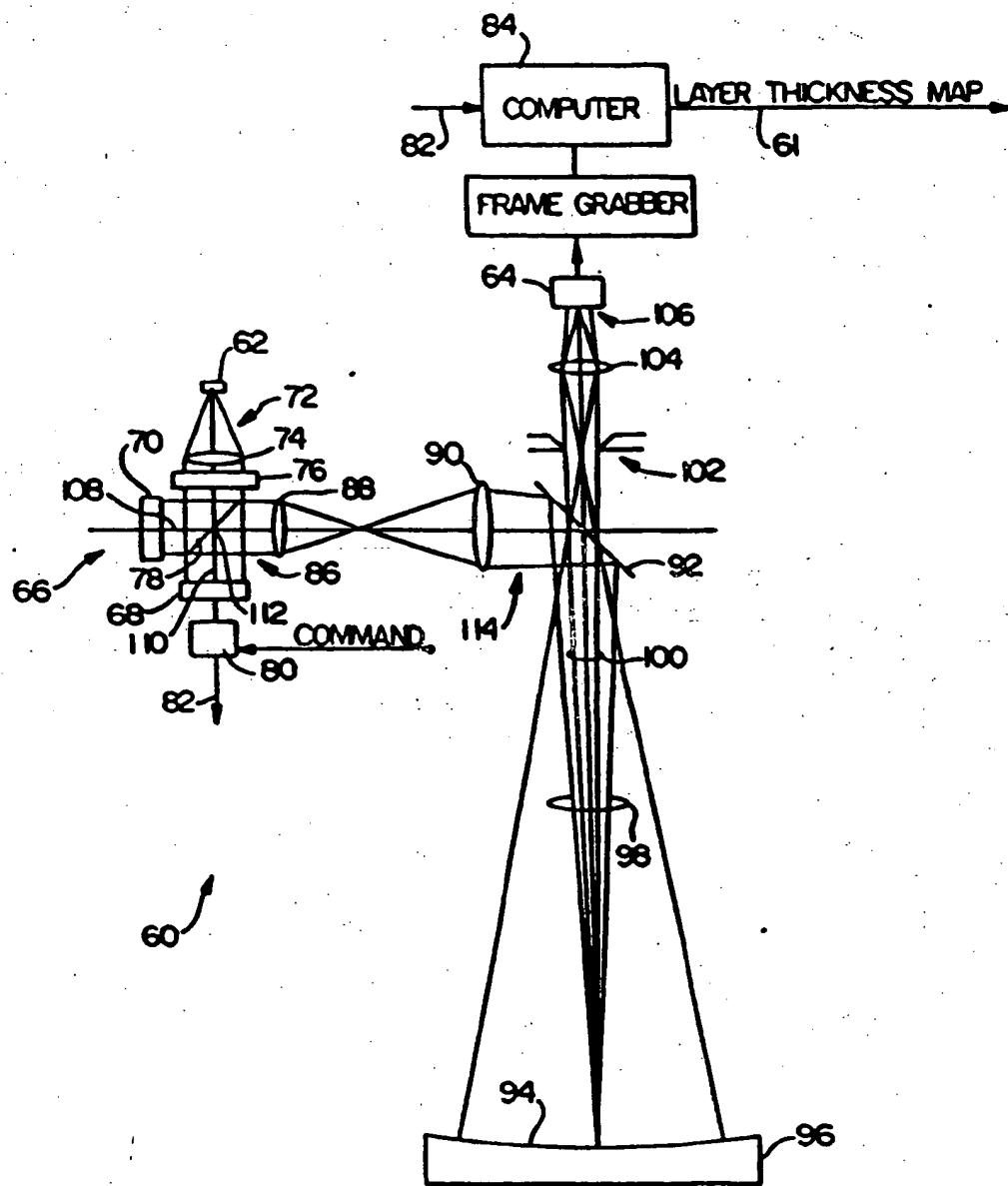


FIG.2

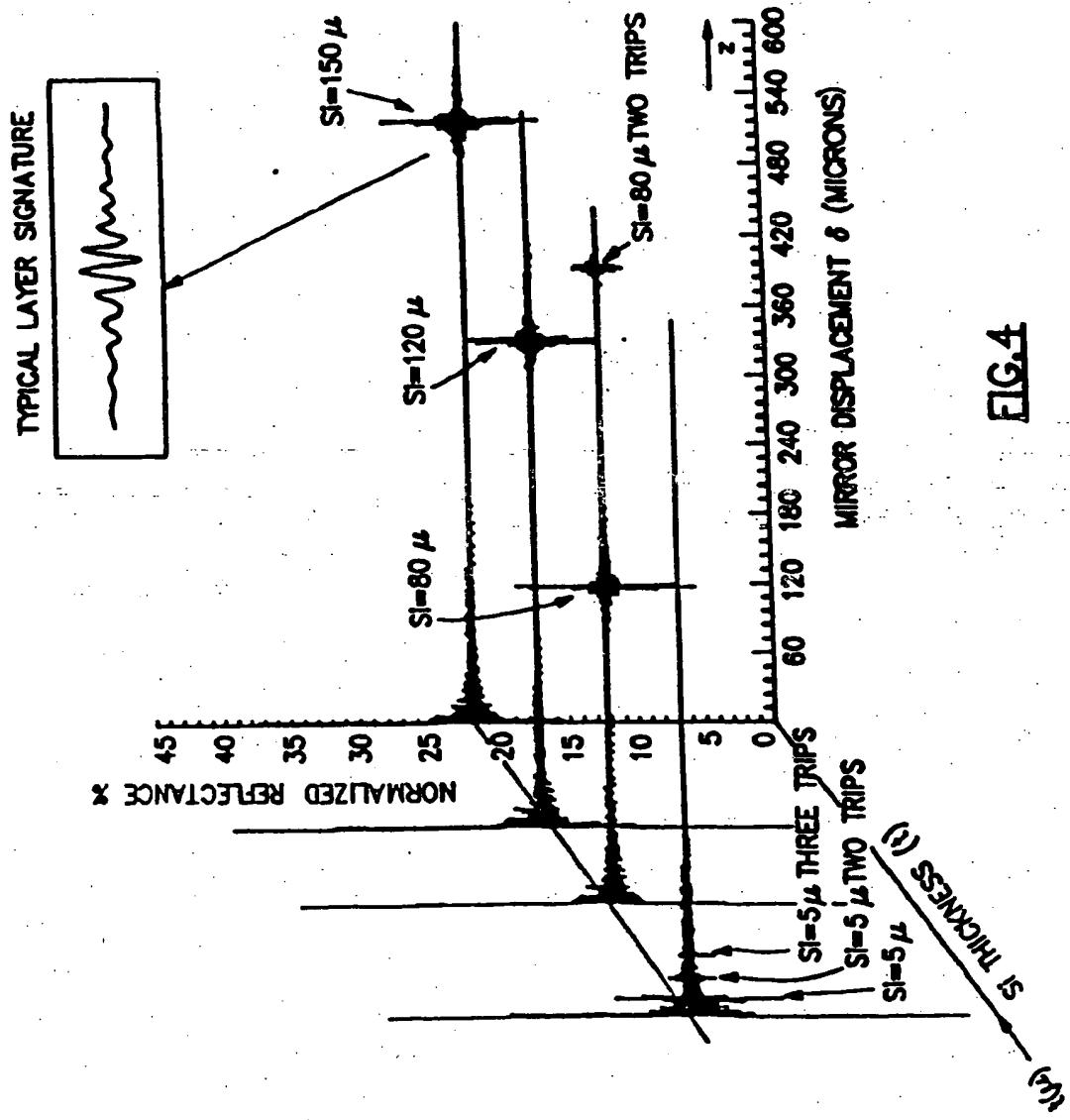


FIG.4

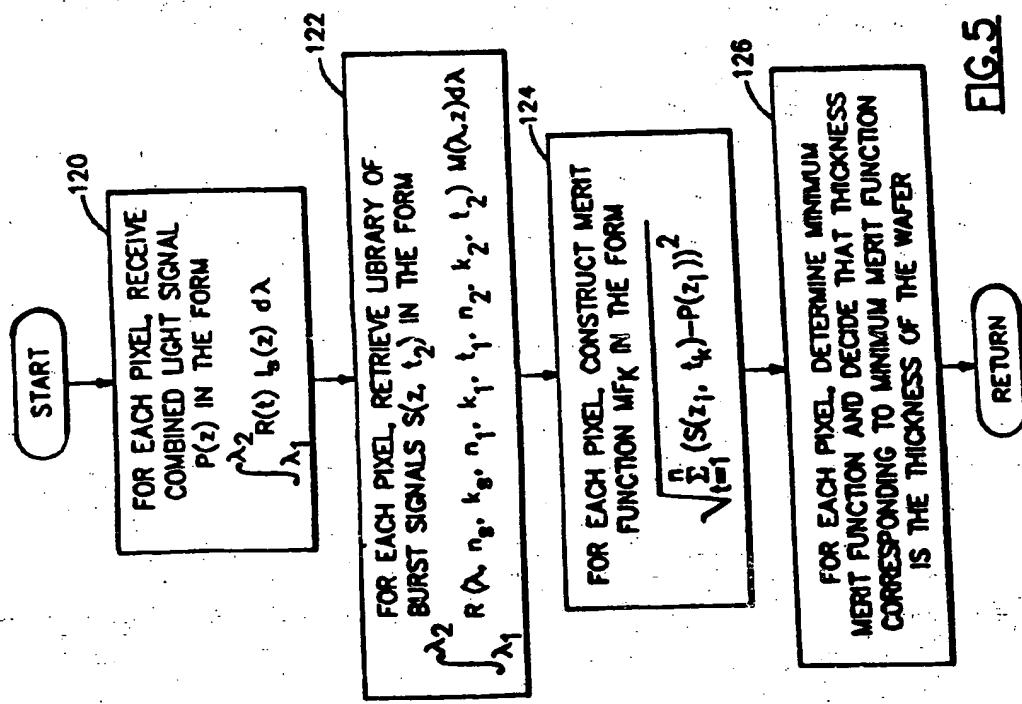


FIG. 5

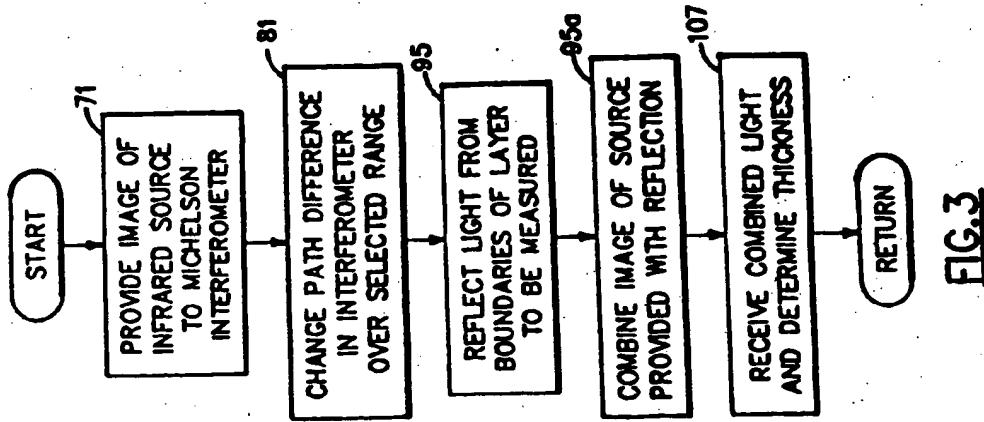


FIG. 3



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## EUROPEAN SEARCH REPORT

Application Number

EP 94 30 2134

DOCUMENTS CONSIDERED TO BE RELEVANT							
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.)				
A	WO-A-90 11487 (VEECO INSTRU INC)		G01B11/06				
<table border="1"> <tr> <td colspan="2">TECHNICAL FIELDS SEARCHED (Int.Cl.)</td> </tr> <tr> <td colspan="2">G01B</td> </tr> </table>				TECHNICAL FIELDS SEARCHED (Int.Cl.)		G01B	
TECHNICAL FIELDS SEARCHED (Int.Cl.)							
G01B							
<p>The present search report has been drawn up for all claims</p>							
Place of search	Date of completion of the search	Examiner					
THE HAGUE	14 July 1994	Batteson, A					
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, not published on, or after the filing date D : document cited in the application I : document cited for other reasons & : member of the same patent family, corresponding document					
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document							